

Abstract of the Disclosure:

A configuration for identifying a switch position of a power switch for microprocessor-controlled appliances is provided. A switch connected in parallel with the power switch is to produce a switch-off delay which has no adverse effect on the interrogation of the switch position. The power switch contains two series-connected switches which can be opened or closed only jointly, with one contact of one switch being connected to one of the two live or neutral conductors, and its other contact being connected to a first input of a sensor which, when voltage is applied and the power switch is switched on, passes a measurement current to the second input of the sensor, which is connected to another one of the two live or neutral conductors. On the output side, the sensor transmits an output signal, corresponding to the switch position of the power switch to a microprocessor for the appliance.

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